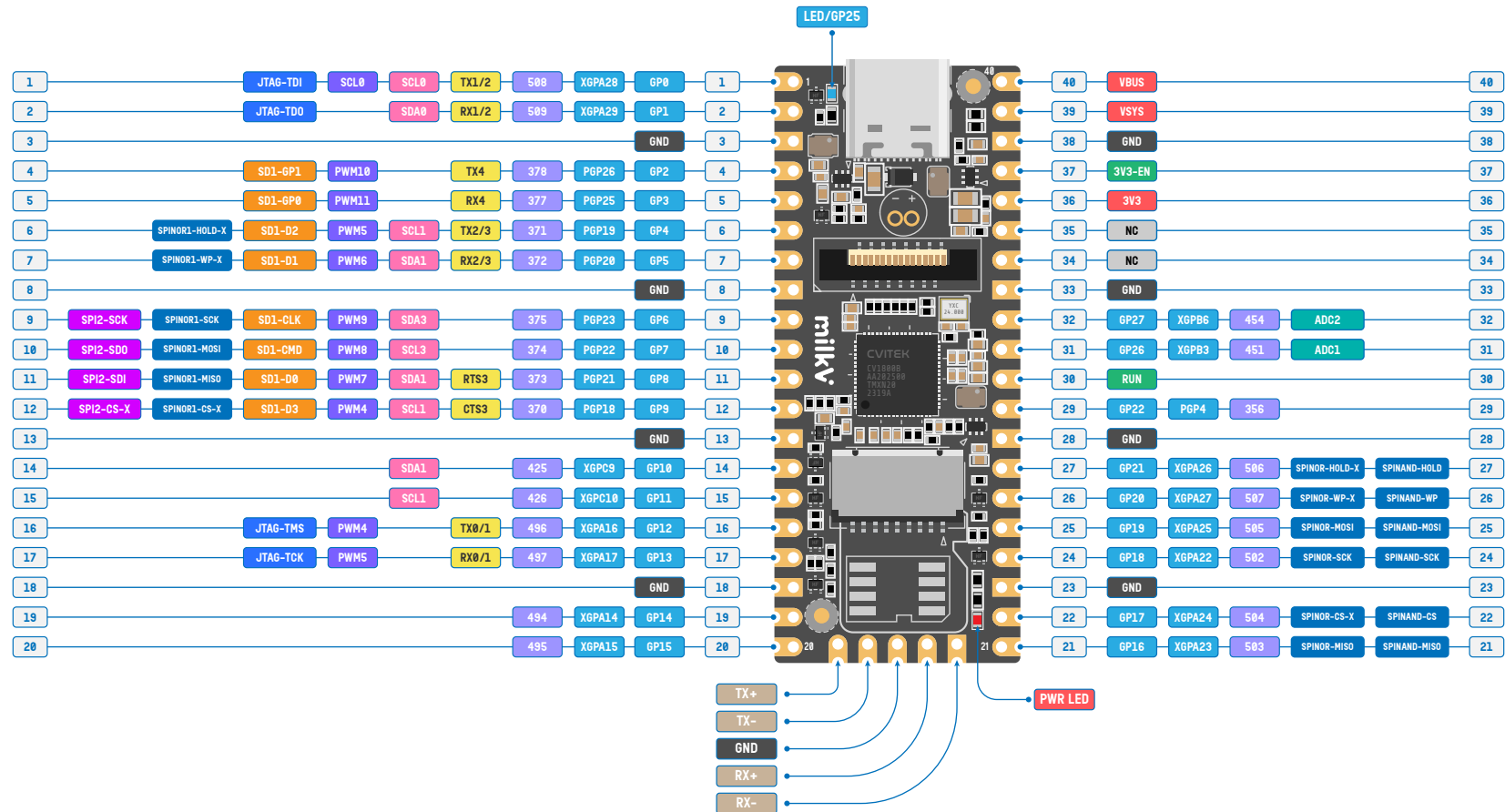


MILK-V DUO - CV1800B PINOUT



PHYSICAL PIN	POSITIVE SUPPLY	SDIO	SPI MEMORY
CONTROL	GROUND SUPPLY	PWM	UART
GPIO	ADC INPUTS	I2C	ETHERNET
JTAG	NUMBER	SPI	NOT CONNECTED

- Default **UART0** mapped to pins 16 & 17 can be used for debugging.
- ADC inputs are 1.8V only.
- Debug LED is connected to GP25 which is not broken out.

Board Name: Milk-V Duo 64 MB
Vendor: Milk-V
Flash: External NAND/NOR
DRAM: 64 MB
CPU: Dual-Core CV1800B RISC-V, 1x C306@1 GHz, 1x C306@700 MHz,
 1x 0.5 TOPS @ INT8 TPUL ISP 4M@25 FPS, 1x 800I MCU with 8KB SRAM,
 1x SPI-NAND, 1xSPI-NOR, 2x SDIO3.0, 1x MIPI 4L CSI, H.265/H.264,
 1x 10M/100M MAC PHY, Secure Boot/AES/SHA/Efuse, 1x 16-Bit Audio,
 3x ADC, 4x I2C, 3x SPI, 5x UART, 4 x PWM, 53x GPIOs.



Rev. 0.1, 07-08-2025
 Design: Vishnu Mohanan
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